A postdoctoral fellowship is offered by the Center for Smart Antenna Systems in the Department of Electrical and Computer Engineering, Brigham Young University, Provo, Utah, USA. The project is in the area of broadband adaptive phased array antennas for terrestrial and satellite communications. Candidates should possess an earned Ph.D. in Electrical Engineering. In-depth experience in electromagnetic analysis and simulation, RF circuit design, microwave systems engineering (1-30 GHz), and board layout using a software package such as Advanced Design System (ADS, Agilent, Inc.) is essential. Experience with phased array antenna design, RF-CMOS chip design and packaging, array signal processing, digital signal processing, and FPGA design is desirable. Interest or experience in technology transfer, commercialization, and entrepreneurship will also be valuable. The individual must be highly motivated and able to mentor a team of graduate students and undergraduates in a fast-paced, interdisciplinary research environment. The position is available for two years with possible extension for a third year, and may lead to full-time employment with the project sponsor. Salary depends on experience. Candidates need not be U.S. citizens but must be eligible to work in the U.S. Review of applications will begin November 15, 2009 and continue until the position is filled. Interested candidates should email a resume, summary of research interests, and letter of recommendation from his or her Ph.D. advisor or recent technical supervisor (the letter may be sent separately) to Prof. Karl F. Warnick at warnick@byu.edu. An online application for the position must also be submitted at yjobs.byu.edu.